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1. Memory  
*Page 8/89*



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This set of computer organization and architecture objective questions include the

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collections of  
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architecture. 1.

Memory unit that  
communicates  
directly with the  
CPU is called the

..... A) Main

memory B)

Secondary memory

# Where To Download

- C) Auxiliary memory
- D) Register. 2.

## Architecture

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on to Computer

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Objectives: The  
objectives of this  
course are: To  
understand how

computers are  
constructed out of a  
set of functional  
units To understand  
how these functional  
units operate,  
interact and  
communicate

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Organization and  
Architecture. In a  $k$ -  
way set associative  
cache, the cache is  
divided into  $v$  sets,  
each of which  
consists of  $k$  lines.



## Where To Download

The lines of a set are placed in sequence one after another. The lines in set  $s$  are sequenced before the lines in set  $(s+1)$ . The main memory blocks are numbered 0 onwards.

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additionally useful.

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...

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architecture . You  
are developing a  
high frequency  
trading system to try  
to beat the stock  
market. Your  
system will require a  
very high hit rate

## Where To Download

with low hit times in a medium-sized cache between main memory and the CPU. Because of the almost unlimited budget your firm has, the hit rate is prioritized over all other ...

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Choice ...~~

2. SIMD represents  
an organization that

\_(A) refers to

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a computer system capable of processing several programs at the same time. (B)

represents organization of single computer containing a control unit, processor unit and a memory unit. (C) includes many processing units



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under the  
supervision of a  
common control unit  
(D) none of the  
above.

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answers. Choose  
your option and  
check it with the  
given correct  
answer.

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2. SIMD represents an organization that  
\_(A) Refers to a computer system capable of processing several programs at the



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same time (B)

Represents

organization of

single computer

containing a control

unit, processor unit

and a memory unit

(C) Includes many

processing units

under the

supervision of a

common control unit

(D) None of the

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Set 01 ...

In computer engineering, computer architecture is a set of rules and methods that describe the

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functionality,  
organization, and  
implementation of  
computer systems.  
Some definitions of  
architecture define it  
as describing the  
capabilities and  
programming model  
of a computer but  
not a particular  
implementation. In  
other definitions

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computer  
architecture involves  
instruction set  
architecture design,  
microarchitecture  
design, logic design,  
and implementation.

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instruction is\_

(A) Branch and

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architect can be a daunting challenge for many. And if the objective statement in your resume doesn't hit the nail on the head instantly, you could lose your chances of getting the job.

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Objective: This

course is intended  
to teach the basics  
involved in data  
representation and  
digital logic circuits  
used in the  
computer system.

This includes the



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general concepts in digital logic design, including logic elements, and their use in combinational and sequential logic circuit design.

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~~L-1.1: Computer  
Organization and~~  
*Page 43/89*

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Organization And  
Syllabus ...

Course Description  
& Objectives:

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architecture is a key  
component of  
computer  
engineering and the  
practicing computer  
engineer should  
have a practical  
understanding of

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this topic. It is concerned with all aspects of the design and organization of the central processing unit and the integration of the CPU into the computer system itself

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organization and  
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~~Organizationa~~



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organization and



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architecture. 1.

Memory unit that communicates directly with the CPU is called the

- A) Main memory
- B) Secondary memory
- C) Auxiliary memory
- D) Register. 2.

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objectives of this

course are: To

understand how

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computers are constructed out of a set of functional units To understand how these functional units operate, interact and communicate

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Architecture. In a  $k$ -way set associative cache, the cache is divided into  $v$  sets, each of which consists of  $k$  lines.

The lines of a set are placed in sequence one after another. The lines in set  $s$  are sequenced

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before the lines in set (s+1). The main memory blocks are numbered 0 onwards.

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architecture . You are developing a high frequency trading system to try to beat the stock market. Your system will require a very high hit rate with low hit times in a medium-sized cache between main memory and the CPU. Because



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of the almost  
unlimited budget  
your firm has, the hit  
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2. SIMD represents an organization that  
\_(A) refers to a computer system capable of processing several programs at the same time. (B)

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Questions With  
Answers

represents  
organization of  
single computer  
containing a control  
unit, processor unit  
and a memory unit.

- (C) includes many  
processing units  
under the  
supervision of a  
common control unit
- (D) none of the  
above.

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answers. Choose  
your option and  
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given correct  
answer.

Memory  
Organization  
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2. SIMD represents  
an organization that

(A) Refers to  
a computer system  
capable of

processing several  
programs at the  
same time (B)

Represents  
organization of  
single computer  
containing a control

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unit, processor unit  
and a memory unit  
(C) Includes many  
processing units  
under the  
supervision of a  
common control unit  
(D) None of the  
above

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Architecture MCQ

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Organization And  
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In computer  
engineering,  
computer  
architecture is a set  
of rules and  
methods that  
describe the  
functionality,  
organization, and  
implementation of  
computer systems.  
Some definitions of

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architecture define it  
as describing the  
capabilities and  
programming model  
of a computer but  
not a particular  
implementation. In  
other definitions  
computer  
architecture involves  
instruction set  
architecture design,  
microarchitecture

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Computer  
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design, logic design,  
and implementation.

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architect can be a  
daunting challenge  
for many. And if the  
objective statement  
in your resume

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doesn't hit the nail  
on the head  
instantly, you could  
lose your chances  
of getting the job.

~~Top 22 Architect  
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Organization  
Objective: This

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course is intended to teach the basics involved in data representation and digital logic circuits used in the

computer system.

This includes the general concepts in digital logic design, including logic elements, and their use in combinational

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and sequential logic  
circuit design.

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Architecture Multiple

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(MCQ) 22. Shuseel  
Baral.

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important for the  
students because  
here you will get  
knowledge of all

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~~L-1.1: Computer  
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~~Syllabus ...~~

Course Description  
& Objectives:

Computer

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Computer architecture is a key component of computer engineering and the practicing computer engineer should have a practical understanding of this topic. It is concerned with all aspects of the design and organization of the



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Organization And  
unit and the  
Architecture  
integration of the  
CPU into the  
Objective Type  
computer system  
Questions With  
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Answers