

Chapter 7 Microcontroller Implementation Consideration

Chapter 7 Implementing Strategies: Management and Operations Issues Chapter-7

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The size of a pointer depends on the architecture of the CPU and the implementation of the C compiler. For more information on the architectureof the TM4C see Chapter 3 of Embedded Systems: Introduction to ARM Cortex M Microcontrollers by Jonathan W. Valvano. Figure 7-2: Memory map of the TM4C123 and TM4C1294.

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